



Specifications

GEN SERIES BASIC 1M XT ISO DIGITIZER

Basic 1M XT iso Digitizer

Analog Input Selection

| | |
|-----------------------------------|--|
| Number of Channels | 8 |
| Input Type | Unbalanced differential ⁽¹⁾ , isolated |
| Input Connectors | Fully insulated BNC |
| Input Ranges | ± 2.0 V, ± 4.0 V, ± 10 V, ± 20 V, ± 40 V, ± 100 V plus variable gain in 1000 steps (0.1 %) |
| Offset (zero position) | 1000 steps (0.1 %) |
| Input Coupling | DC, GND |
| Input Impedance | 1 MΩ (± 1 %) // 55 pF (± 10 %) |
| Maximum Static Error (MSE) | 0.1 % full scale |
| Noise | 0.02 % full scale |
| Analog Bandwidth | 500 kHz (-3 dB) |
| CMRR | > 72 dB @ 100 Hz |
| Overload Protection | 250 |
| Number of Slots | 1, incl. signal conditioners |

Isolation

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|---------------------------|--------------------------|
| Channel-to-chassis | 250 V |
| Channel-to-channel | 250 V |
| Non-destructive | 250 V to chassis (earth) |

Acquisition

| | |
|-----------------------------------|---|
| Sample Rate | From 1 MS/s to 0.1 S/s |
| ADC Resolution | 16 bit (0.0015 %) |
| Anti-Alias Filters | Bypass, Time-, Frequency- domain optimized |
| Time Domain | 7-pole Bessel 220 kHz, optimal step response |
| Frequency Domain | 7-pole Butterworth 370 kHz, extended frequency response |
| Digital Decimation Filters | Off, Frequency domain optimized |
| Frequency Domain | 12-pole FIR at sample rate divided by 4, 10, 20, 40 |

Transient Memory

Standard 256 MS per card, shared by enabled channels.

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|-------------------|-------------------|
| 8 channels | 32 MS per channel |
|-------------------|-------------------|

Triggering

Each channel has individual dual-level trigger detection with selectable hysteresis, modes and qualifiers.

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|------------------------------|--|
| Pre- and post-trigger | 0 to full memory length |
| Trigger Rate | Up to 1000 triggers per second, zero re-arm time |
| Resolution | 16 bit for each level (= 0.0015 %) |

STATSTREAM® Real-time Analysis

Each channel includes real-time extraction of Max, Min, Mean, Peak-to-peak, and RMS values.

Acquisition Modes

| | |
|-------------------|--|
| Sweeps | Triggered acquisition to RAM without sample rate limitations; for single or repetitive transients or intermittent phenomena |
| Continuous | Direct storage to PC or mainframe hard disc without file size limitations; triggered or untriggered; for long duration recorder type applications with up to 1 MS/s rate per channel; (maximum aggregate rate pending from mainframe configuration and PC) |
| Dual | Combination of Sweeps and Continuous; recorder type streaming to hard disc with simultaneously triggered sweeps in RAM |

Ordering Information

| | |
|-----------------------|------------------------------|
| Basic1M iso XT | 1-GN813-2 (ex 845-081100) |
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(1) An unbalanced differential input can be used to do differential, off ground, isolated measurements like a "real" differential input. The difference is the implementation using an unbalanced isolated circuitry rather than using a balanced differential one.

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